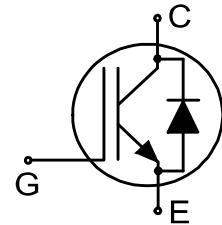


High speed DuoPack: IGBT in Trench and Fieldstop technology with soft, fast recovery anti-parallel diode

Features:

TRENCHSTOP™ technology offering

- very low V_{CEsat}
- low EMI
- Very soft, fast recovery anti-parallel diode
- maximum junction temperature 175°C
- qualified according to JEDEC for target applications
- Pb-free lead plating; RoHS compliant
- complete product spectrum and PSpice Models:
<http://www.infineon.com/igbt/>



Applications:

- uninterruptible power supplies
- welding converters
- converters with high switching frequency



Key Performance and Package Parameters

Type	V_{CE}	I_c	$V_{CEsat}, T_{vj}=25^\circ C$	T_{vjmax}	Marking	Package
IKW30N60H3	600V	30A	1.95V	175°C	K30H603	PG-T0247-3

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Maximum ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage, $T_{vj} \geq 25^\circ\text{C}$	V_{CE}	600	V
DC collector current, limited by T_{vjmax} $T_C = 25^\circ\text{C}$ $T_C = 100^\circ\text{C}$	I_C	60.0 30.0	A
Pulsed collector current, t_p limited by T_{vjmax}	I_{Cpuls}	120.0	A
Turn off safe operating area $V_{CE} \leq 600\text{V}$, $T_{vj} \leq 175^\circ\text{C}$, $t_p = 1\mu\text{s}$	-	120.0	A
Diode forward current, limited by T_{vjmax} $T_C = 25^\circ\text{C}$ $T_C = 100^\circ\text{C}$	I_F	30.0 15.0	A
Diode pulsed current, t_p limited by T_{vjmax}	I_{Fpuls}	120.0	A
Gate-emitter voltage	V_{GE}	± 20	V
Short circuit withstand time $V_{GE} = 15.0\text{V}$, $V_{CC} \leq 400\text{V}$ Allowed number of short circuits < 1000 Time between short circuits: $\geq 1.0\text{s}$ $T_{vj} = 150^\circ\text{C}$	t_{sc}	5	μs
Power dissipation $T_C = 25^\circ\text{C}$ Power dissipation $T_C = 100^\circ\text{C}$	P_{tot}	187.0 94.0	W
Operating junction temperature	T_{vj}	-40...+175	$^\circ\text{C}$
Storage temperature	T_{stg}	-55...+150	$^\circ\text{C}$
Soldering temperature, wave soldering 1.6 mm (0.063 in.) from case for 10s		260	$^\circ\text{C}$
Mounting torque, M3 screw Maximum of mounting processes: 3	M	0.6	Nm

Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				
IGBT thermal resistance, junction - case	$R_{th(j-c)}$		0.80	K/W
Diode thermal resistance, junction - case	$R_{th(j-c)}$		1.90	K/W
Thermal resistance junction - ambient	$R_{th(j-a)}$		40	K/W

Electrical Characteristic, at $T_{vj} = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(\text{BR})\text{CES}}$	$V_{\text{GE}} = 0\text{V}, I_{\text{C}} = 2.00\text{mA}$	600	-	-	V
Collector-emitter saturation voltage	V_{CEsat}	$V_{\text{GE}} = 15.0\text{V}, I_{\text{C}} = 30.0\text{A}$ $T_{vj} = 25^\circ\text{C}$ $T_{vj} = 125^\circ\text{C}$ $T_{vj} = 175^\circ\text{C}$	-	1.95	2.40	V
Diode forward voltage	V_F	$V_{\text{GE}} = 0\text{V}, I_F = 15.0\text{A}$ $T_{vj} = 25^\circ\text{C}$ $T_{vj} = 125^\circ\text{C}$ $T_{vj} = 175^\circ\text{C}$	-	1.65	2.05	V
Gate-emitter threshold voltage	$V_{\text{GE}(\text{th})}$	$I_{\text{C}} = 0.43\text{mA}, V_{\text{CE}} = V_{\text{GE}}$	4.1	5.1	5.7	V
Zero gate voltage collector current	I_{CES}	$V_{\text{CE}} = 600\text{V}, V_{\text{GE}} = 0\text{V}$ $T_{vj} = 25^\circ\text{C}$ $T_{vj} = 175^\circ\text{C}$	-	-	40.0	μA
Gate-emitter leakage current	I_{GES}	$V_{\text{CE}} = 0\text{V}, V_{\text{GE}} = 20\text{V}$	-	-	100	nA
Transconductance	g_{fs}	$V_{\text{CE}} = 20\text{V}, I_{\text{C}} = 30.0\text{A}$	-	16.0	-	S

Electrical Characteristic, at $T_{vj} = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Dynamic Characteristic						
Input capacitance	C_{ies}		-	1630	-	pF
Output capacitance	C_{oes}	$V_{\text{CE}} = 25\text{V}, V_{\text{GE}} = 0\text{V}, f = 1\text{MHz}$	-	107	-	
Reverse transfer capacitance	C_{res}		-	50	-	
Gate charge	Q_G	$V_{\text{CC}} = 480\text{V}, I_{\text{C}} = 30.0\text{A}, V_{\text{GE}} = 15\text{V}$	-	165.0	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	L_E		-	13.0	-	nH
Short circuit collector current Max. 1000 short circuits Time between short circuits: $\geq 1.0\text{s}$	$I_{\text{C}(\text{SC})}$	$V_{\text{GE}} = 15.0\text{V}, V_{\text{CC}} \leq 400\text{V}, t_{\text{sc}} \leq 5\mu\text{s}, T_{vj} = 150^\circ\text{C}$	-	160	-	A

Switching Characteristic, Inductive Load

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic, at $T_{vj} = 25^\circ\text{C}$						
Turn-on delay time	$t_{d(on)}$	$T_{vj} = 25^\circ\text{C}$, $V_{CC} = 400\text{V}$, $I_C = 30.0\text{A}$, $V_{GE} = 0.0/15.0\text{V}$, $r_G = 10.5\Omega$, $L\sigma = 95\text{nH}$, $C\sigma = 67\text{pF}$ $L\sigma$, $C\sigma$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	21	-	ns
Rise time	t_r		-	33	-	ns
Turn-off delay time	$t_{d(off)}$		-	207	-	ns
Fall time	t_f		-	22	-	ns
Turn-on energy	E_{on}		-	0.94	-	mJ
Turn-off energy	E_{off}		-	0.44	-	mJ
Total switching energy	E_{ts}		-	1.38	-	mJ
Diode reverse recovery time	t_{rr}	$T_{vj} = 25^\circ\text{C}$, $V_R = 400\text{V}$, $I_F = 15.0\text{A}$, $di_F/dt = 1000\text{A}/\mu\text{s}$	-	38	-	ns
Diode reverse recovery charge	Q_{rr}		-	0.32	-	μC
Diode peak reverse recovery current	I_{frm}		-	12.0	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	-765	-	$\text{A}/\mu\text{s}$

Switching Characteristic, Inductive Load

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic, at $T_{vj} = 175^\circ\text{C}$						
Turn-on delay time	$t_{d(on)}$	$T_{vj} = 175^\circ\text{C}$, $V_{CC} = 400\text{V}$, $I_C = 30.0\text{A}$, $V_{GE} = 0.0/15.0\text{V}$, $r_G = 10.5\Omega$, $L\sigma = 95\text{nH}$, $C\sigma = 67\text{pF}$ $L\sigma$, $C\sigma$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	20	-	ns
Rise time	t_r		-	30	-	ns
Turn-off delay time	$t_{d(off)}$		-	239	-	ns
Fall time	t_f		-	23	-	ns
Turn-on energy	E_{on}		-	1.12	-	mJ
Turn-off energy	E_{off}		-	0.60	-	mJ
Total switching energy	E_{ts}		-	1.72	-	mJ
Diode reverse recovery time	t_{rr}	$T_{vj} = 175^\circ\text{C}$, $V_R = 400\text{V}$, $I_F = 15.0\text{A}$, $di_F/dt = 1000\text{A}/\mu\text{s}$	-	117	-	ns
Diode reverse recovery charge	Q_{rr}		-	1.08	-	μC
Diode peak reverse recovery current	I_{frm}		-	16.6	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	-530	-	$\text{A}/\mu\text{s}$

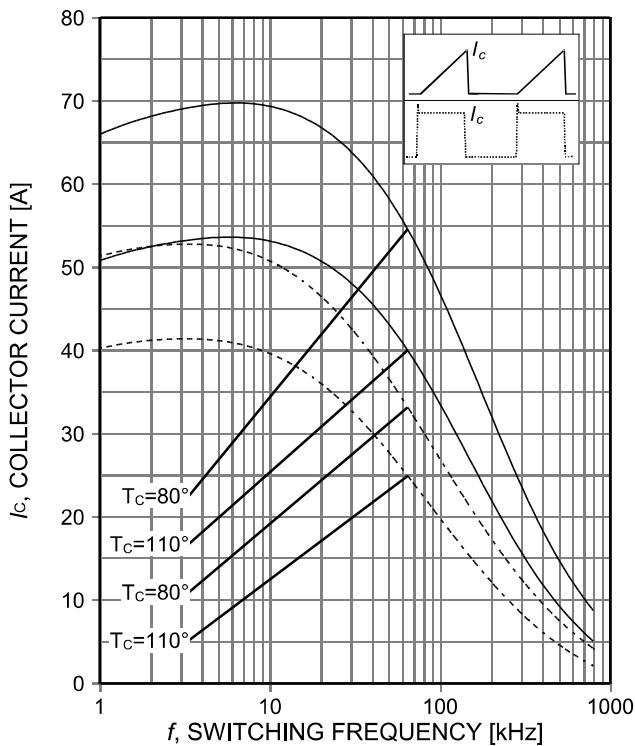


Figure 1. Collector current as a function of switching frequency
 $(T_j \leq 175^\circ\text{C}, D=0.5, V_{CE}=400\text{V}, V_{GE}=15/0\text{V}, r_G=10.5\Omega)$

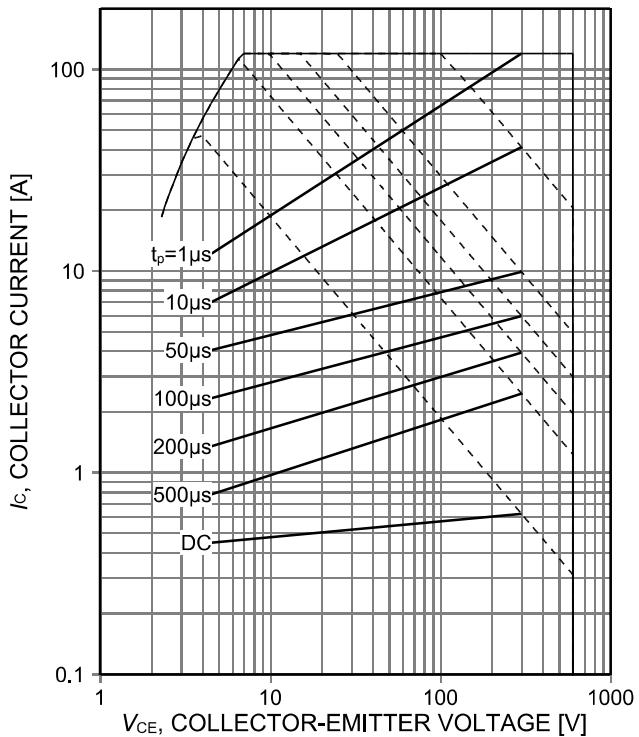


Figure 2. Forward bias safe operating area
 $(D=0, T_c=25^\circ\text{C}, T_j \leq 175^\circ\text{C}; V_{GE}=15\text{V})$

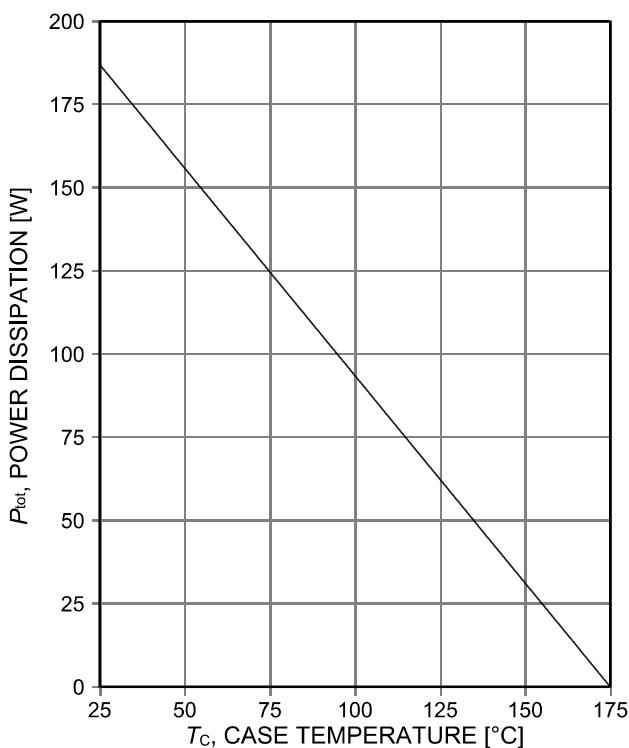


Figure 3. Power dissipation as a function of case temperature
 $(T_j \leq 175^\circ\text{C})$

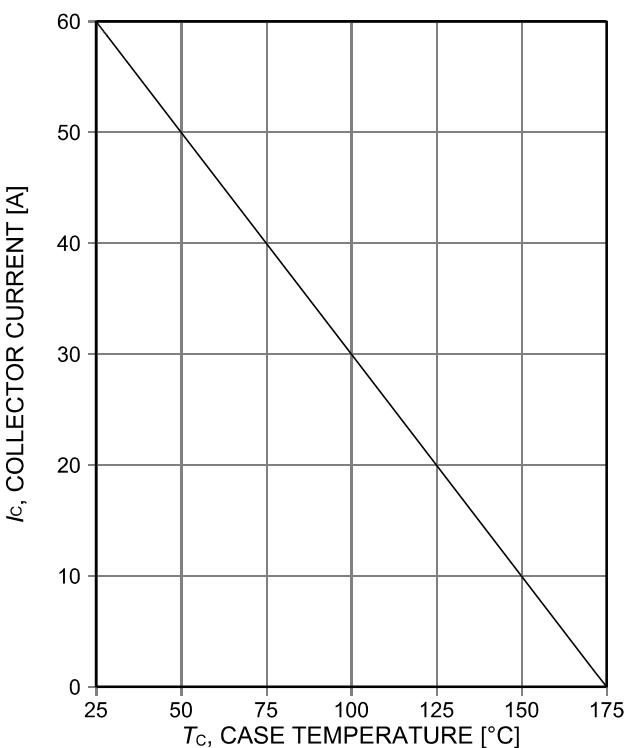


Figure 4. Collector current as a function of case temperature
 $(V_{GE} \geq 15\text{V}, T_j \leq 175^\circ\text{C})$

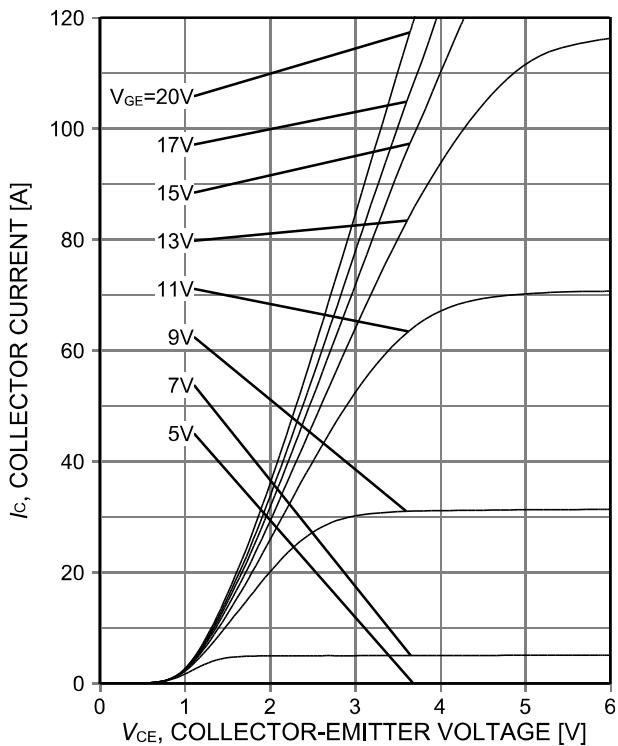


Figure 5. Typical output characteristic
($T_j=25^\circ\text{C}$)

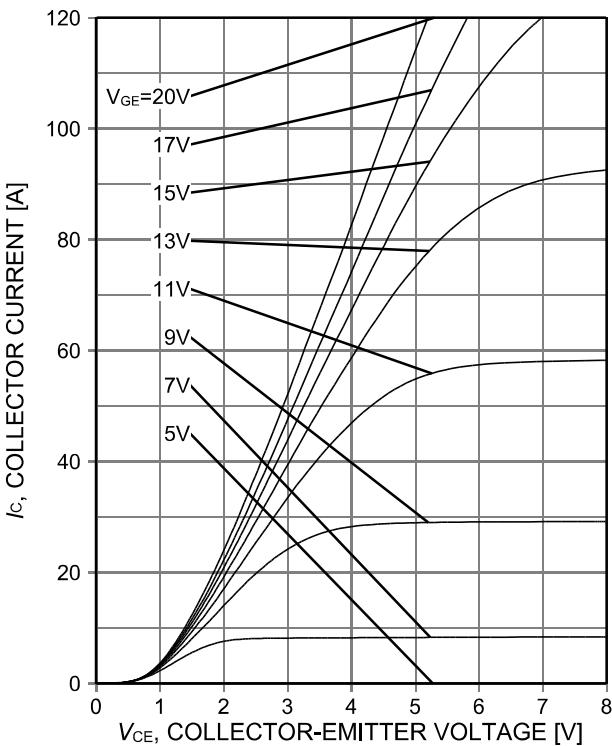


Figure 6. Typical output characteristic
($T_j=175^\circ\text{C}$)

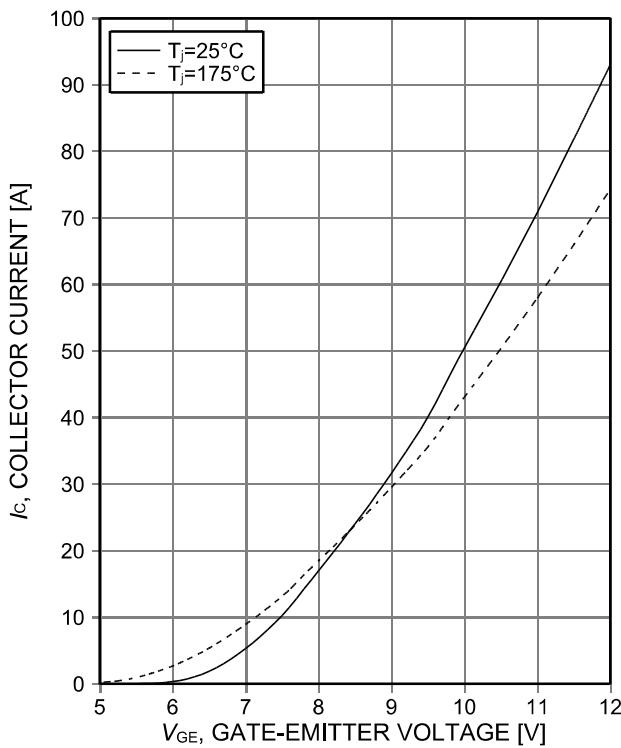


Figure 7. Typical transfer characteristic
($V_{CE}=20\text{V}$)

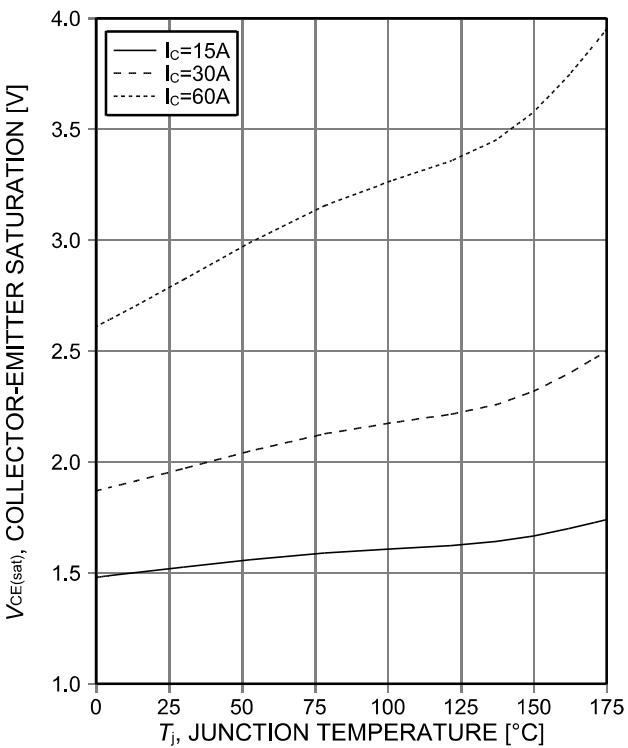


Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature
($V_{GE}=15\text{V}$)

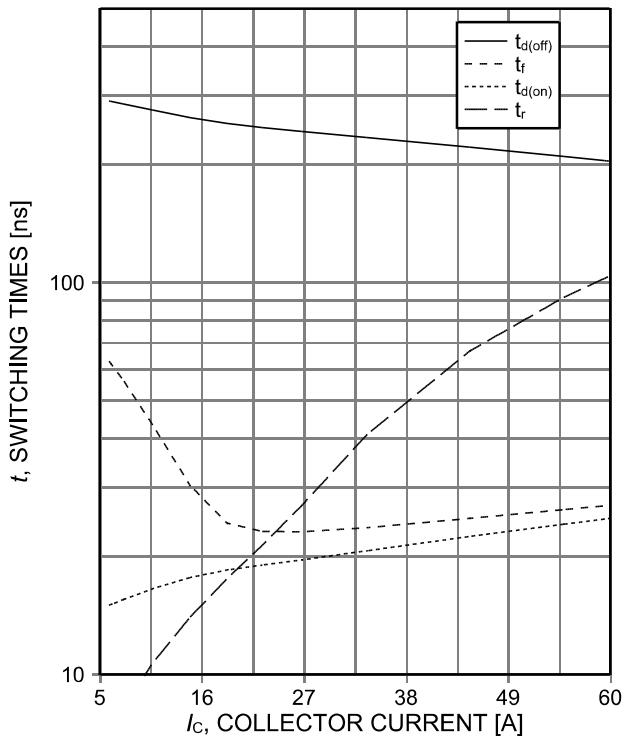


Figure 9. Typical switching times as a function of collector current
(ind. load, $T_j=175^\circ\text{C}$, $V_{CE}=400\text{V}$, $V_{GE}=15/0\text{V}$, $r_G=10.5\Omega$, test circuit in Fig. E)

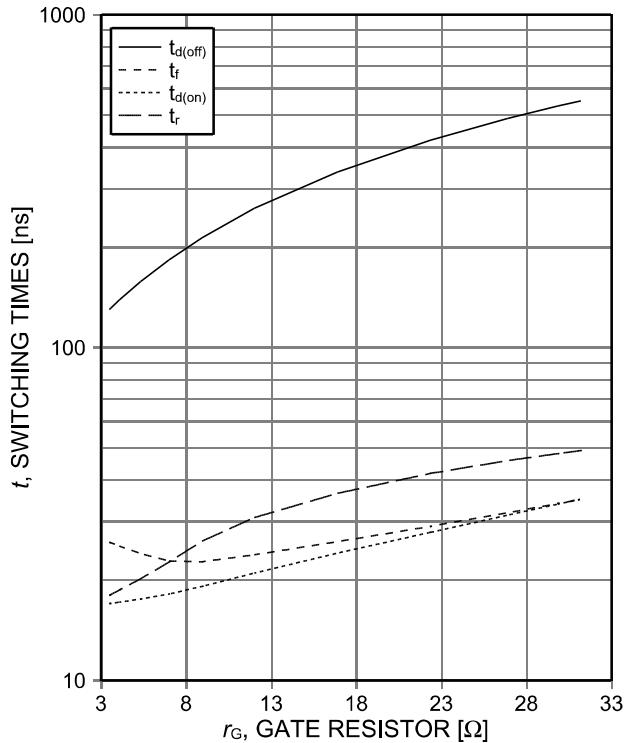


Figure 10. Typical switching times as a function of gate resistor
(ind. load, $T_j=175^\circ\text{C}$, $V_{CE}=400\text{V}$, $V_{GE}=15/0\text{V}$, $I_c=30\text{A}$, test circuit in Fig. E)

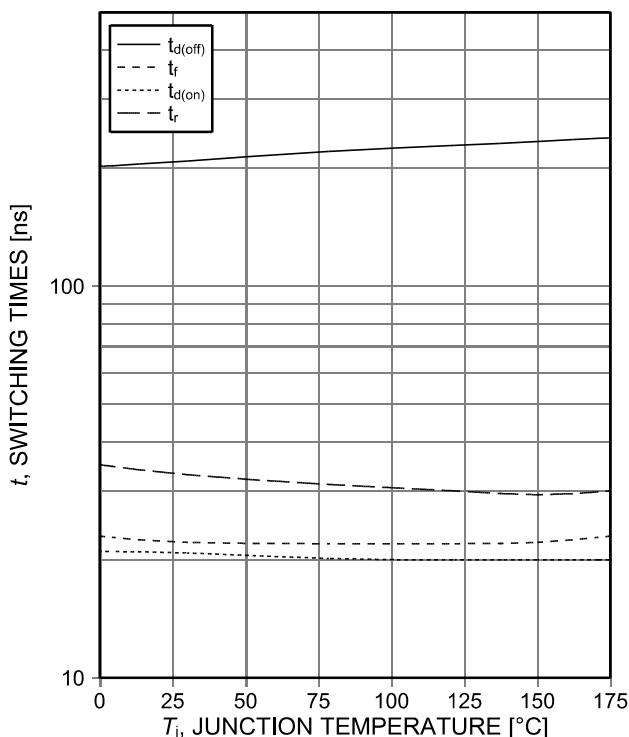


Figure 11. Typical switching times as a function of junction temperature
(ind. load, $V_{CE}=400\text{V}$, $V_{GE}=15/0\text{V}$, $I_c=30\text{A}$, $r_G=10.5\Omega$, test circuit in Fig. E)

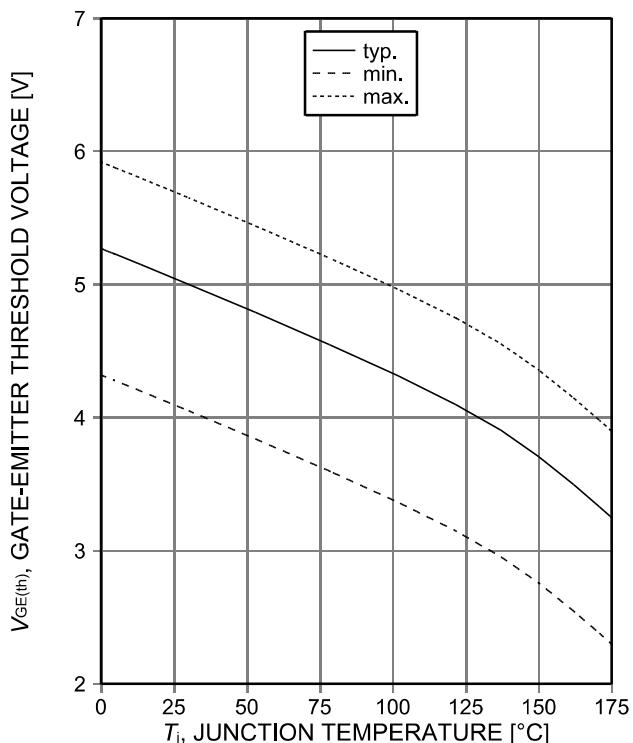


Figure 12. Gate-emitter threshold voltage as a function of junction temperature
($I_c=0.43\text{mA}$)

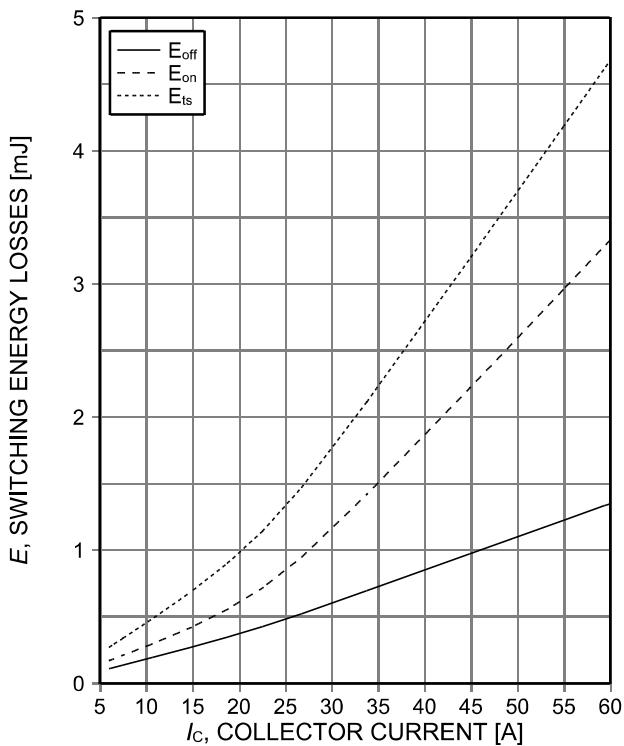


Figure 13. Typical switching energy losses as a function of collector current
(ind. load, $T_j=175^\circ\text{C}$, $V_{CE}=400\text{V}$, $V_{GE}=15/0\text{V}$, $r_G=10,5\Omega$, test circuit in Fig. E)

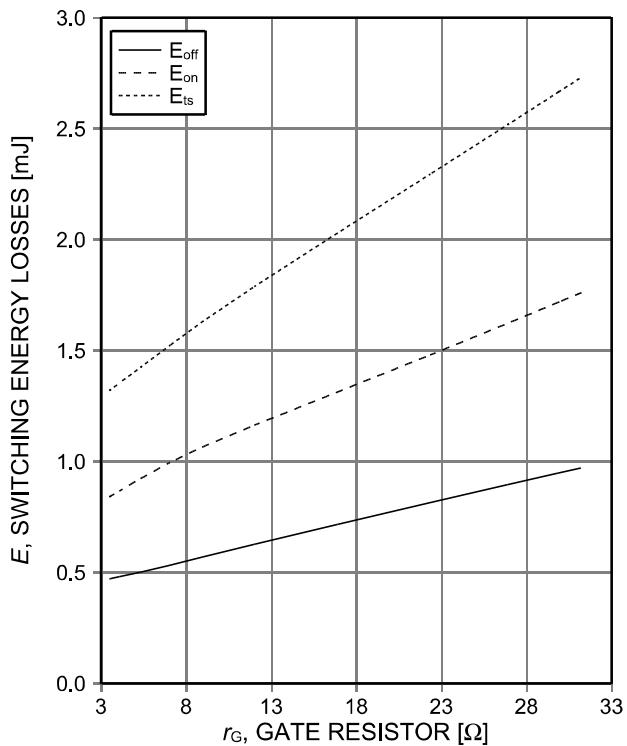


Figure 14. Typical switching energy losses as a function of gate resistor
(ind. load, $T_j=175^\circ\text{C}$, $V_{CE}=400\text{V}$, $V_{GE}=15/0\text{V}$, $I_c=30\text{A}$, test circuit in Fig. E)

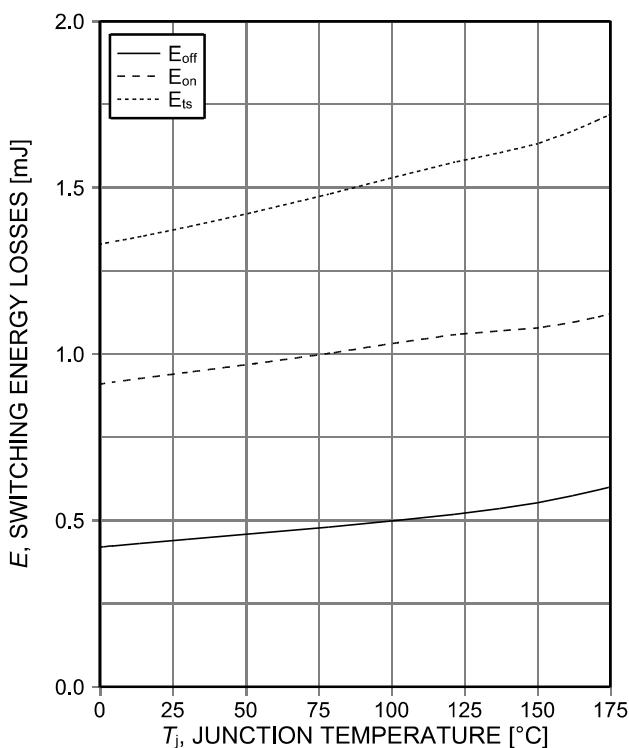


Figure 15. Typical switching energy losses as a function of junction temperature
(ind load, $V_{CE}=400\text{V}$, $V_{GE}=15/0\text{V}$, $I_c=30\text{A}$, $r_G=10,5\Omega$, test circuit in Fig. E)

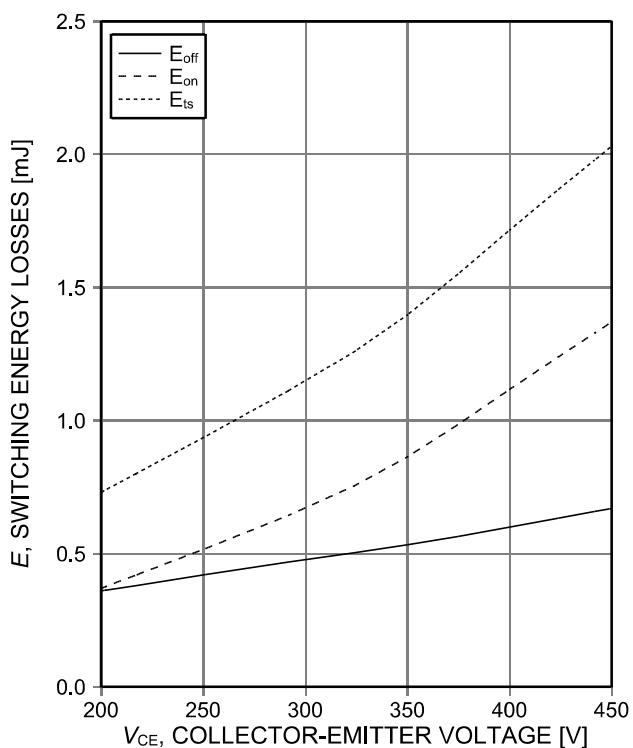


Figure 16. Typical switching energy losses as a function of collector-emitter voltage
(ind. load, $T_j=175^\circ\text{C}$, $V_{GE}=15/0\text{V}$, $I_c=30\text{A}$, $r_G=10,5\Omega$, test circuit in Fig. E)